

FIG. 1

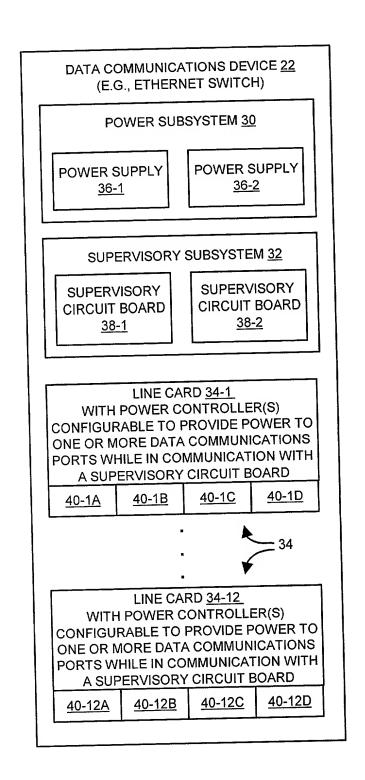
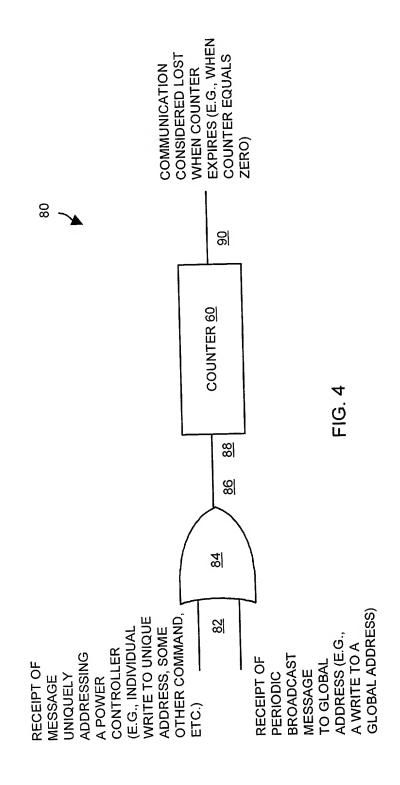


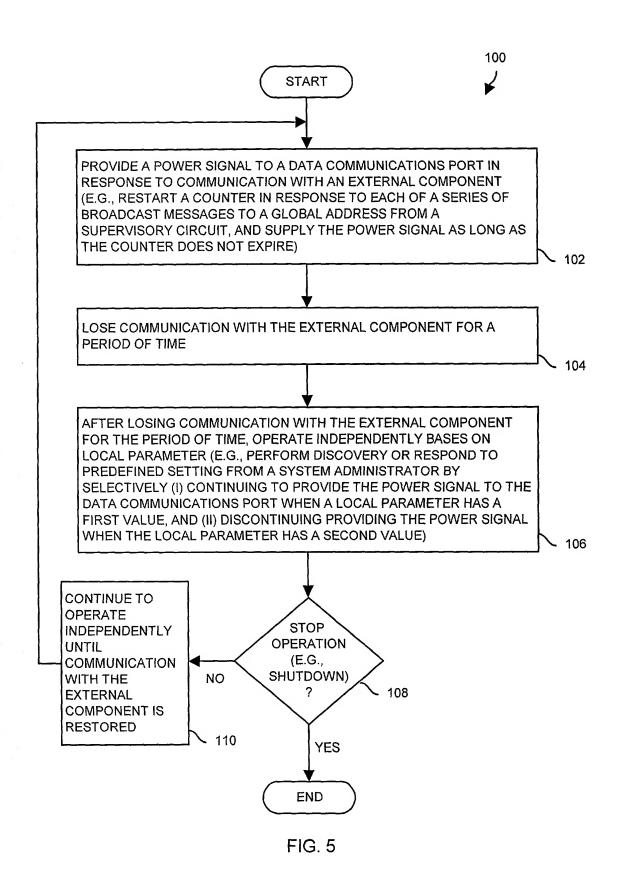
FIG. 2

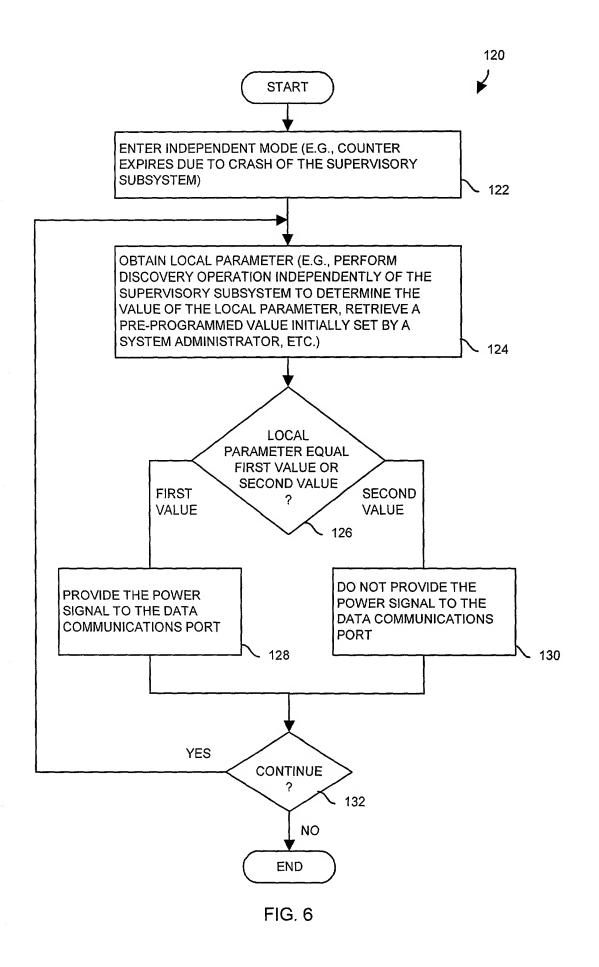


LINE C	ARD <u>34</u>						
POWER SUPPLY CONNECTION <u>50</u>	SUPERVISORY CIRCUIT BOARD INTERFACE <u>52</u>						
POWER CONTROLLER <u>54</u>							
PROCESSOR <u>56</u>							
MEMC	DRY <u>58</u>						
O.S. <u>62</u>	PARAM. <u>66</u>						
APPL. <u>64</u>							
cour	NTER <u>60</u>						
DATA COMMUNIC	CATIONS PORT <u>40-1</u>						
DATA COMMUNIO	CATIONS PORT <u>40-2</u>						
DATA COMMUNI	CATIONS PORT <u>40-3</u>						
DATA COMMUNI	CATIONS PORT <u>40-4</u>						

FIG. 3







SUPERVISORY CIRCUIT 140

INTERFACE 142

CONTROL CIRCUIT 144 CONFIGURED TO PROVIDE A SERIES OF BROADCAST MESSAGES TO A GLOBAL ADDRESS TO MAINTAIN COMMUNICATION WITH MULTIPLE POWER CONTROLLERS

FIG. 7

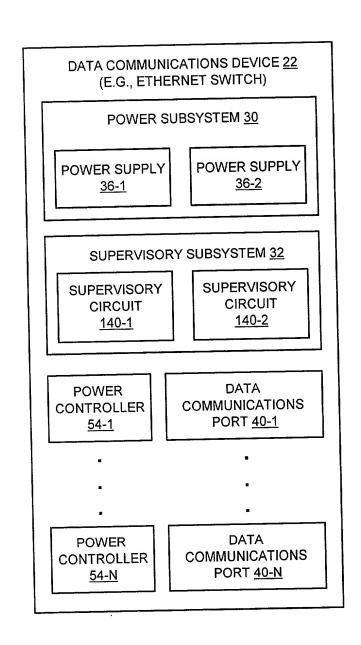


FIG. 8

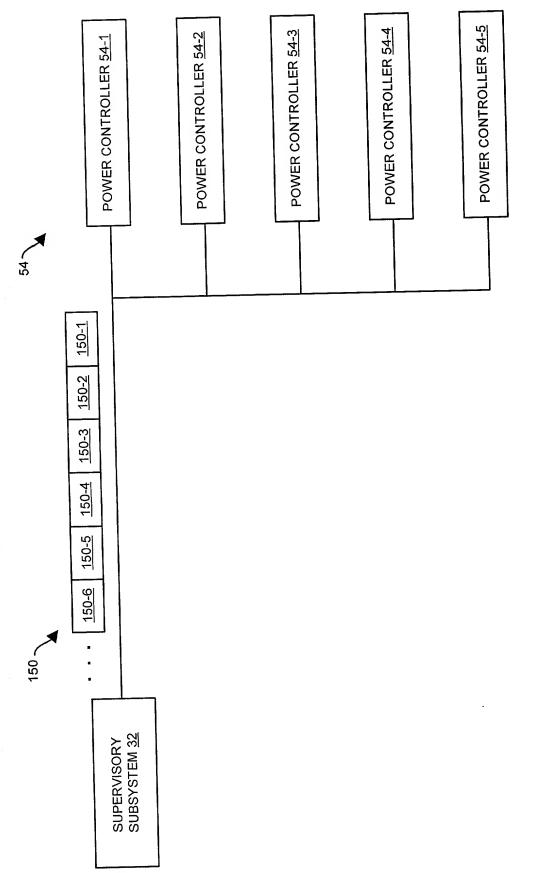


FIG. 9

EFFECT ON POWER CONTROLLER <u>54-1</u> WHEN THE POWER CONTROLLER IS CONFIGURED TO RESPOND TO TRANSACTIONS UNIQUELY	ADDRESSING THE POWER CONTROLLER AND TO TRANSACTIONS TO THE GLOBAL ADDRESS	COMMUNICATION WITH SUPERVISORY SUBSYSTEM MAINTAINED (E.G., RESET COUNTER)	NO RESPONSE	COMMUNICATION WITH SUPERVISORY SUBSYSTEM MAINTAINED (E.G., RESET COUNTER)	COMMUNICATION WITH SUPERVISORY SUBSYSTEM MAINTAINED (E.G., RESET COUNTER)	COMMUNICATION WITH SUPERVISORY SUBSYSTEM MAINTAINED (E.G., RESET COUNTER)	NO RESPONSE	
160	DESCRIPTION	ADDRESS = POWER CONTROLLER 54-1 COMMAND = SETUP REGISTERS	ADDRESS = POWER CONTROLLER <u>54-2</u> COMMAND = SETUP REGISTERS	ADDRESS = POWER CONTROLLER 54-1 COMMAND = POWER PORT	ADDRESS = POWER CONTROLLER <u>54-1</u> COMMAND = READ STATUS	ADDRESS = GLOBAL_ COMMAND = RESET COUNTER	ADDRESS = POWER CONTROLLER 54-3 COMMAND = SETUP REGISTERS	
	TRANSACTION	150-1	150-2	150-3	150-4	150-5	<u>150-6</u>	

FIG. 10